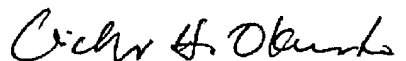


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Alexandria, VA 22313-1450**FAX NO.:** (703) 872-9306  
USPTO Central Fax NumberConfirmation No.: 8820  
Group Art Unit: 2816  
Examiner: Cox, Cassandra F.**FROM:** Victor H. Okumoto  
Reg. No. 35,973**PAGE 1 OF 15.****RE: ELECTION/RESTRICTIONS**

Application Ser. No.: 10/621,055

"Clock Signal Processing Logic and Method for Determining Clock  
Signal Characteristics in Reference Voltage and Temperature Varying  
Environments," Talledo, Cesar et al., filed 07/15/2003  
(Atty. Docket IDTI.002US1-1826)**MESSAGE:** Please find attached, a Non-Fee Amendment responding to the Office  
Action of 08/25/2004 for this case.

Victor H. Okumoto

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Talledo, Cesar A. et al.

Applic. Serial No.: 10/621,055

Filed: 07/15/2003

For: CLOCK SIGNAL PROCESSING  
LOGIC AND METHOD FOR  
DETERMINING CLOCK SIGNAL  
CHARACTERISTICS IN  
REFERENCE VOLTAGE AND  
TEMPERATURE VARYING  
ENVIRONMENTS

Confirmation No.: 8820

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Examiner: Cox, Cassandra F.

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ELECTION/RESTRICTIONS

Sir:

This communication is in response to the Office Action mailed August 25, 2004.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 14 of this paper.

Applic. Serial No. 10/621,055

Page 1 of 14

Atty. Docket No. IDT1.002US1-1826  
August 31, 2004